

Akbarjon Kamoldinov

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EDUCATION

Bachelor of Science in Electrical Engineering

University of Missouri – Columbia

Expected May 2027

Columbia, MO

EXPERIENCE

Substation Engineering Intern

GFT (Gannett Fleming TransSystems)

May 2026 – Aug 2026

St. Louis, MO

- Supporting SMUD Station J, a 10.3-acre downtown Sacramento substation planned for up to six 40 MVA 115/21 kV transformers and up to 9 miles of 115/21 kV overhead/underground connections.
- Helped prepare Bill of Materials (BOM) support for ABB FT test switches in protection/control panels, mapping relay and metering test-switch configurations to design drawings and material lists.
- Reviewed FirstEnergy Iliff LTIIIP III 48 VDC outdoor distribution RTU replacement package, tracing 13.2/7.62 kV one-line, SCADA I/O, Bank 1 switchgear, conduit, grounding, and circuit/nameplate dependencies.

AI Image Engineer

Precision and Automated Agriculture Lab, University of Missouri

Jan 2026 – Present

Columbia, MO

- Built OAK-D ToF computer-vision pipelines for sow posture classification, aligning RGB/depth frames with DepthAI and OpenCV.
- Benchmarked MobileNetV2, Xception, and DenseNet121; achieved 98.42% test accuracy on held-out pigs with DenseNet121/depth.
- Ran production inference over 75,120 frames from 3,766 timestamp folders, generating per-pig heatmaps, timestamp summaries, and a Colab handoff notebook for lab use.

Undergraduate Researcher – AI & LLM Systems

Distributed and Intelligent Computing Lab, University of Missouri

Jan 2025 – Present

Columbia, MO

- Designed reasoning-centered AI systems including LLM chatbots and ARC puzzle-solver prototypes for compositional generalization research.
- Reviewed and implemented ARC-AGI-2 benchmark methods while evaluating solver architectures for machine reasoning with foundation models.

Electrical Engineer Intern

NazarX – Aerospace R&D Center

Jun 2024 – Aug 2024

Tashkent, Uzbekistan

- Prototyped 10+ nanosatellite and drone components in SolidWorks, reducing estimated production cost by ~15%.
- Performed tolerance and stress simulations to meet ISO 2768 and FAA Part 23 design criteria; led CubeSat training for 30+ students from the UAE.

PROJECTS

Hiroo – AI Job-Search Agent

Founder / CTO

2026

hiroo.me

- Built an AI job-search agent that ingests resumes and goals, drafts targeted outreach with Gemini, connects Gmail OAuth, tracks replies, and runs customer/admin dashboards with Netlify Functions.
- Grew launch waitlist to 200+ users in 48 hours through Telegram and cold DMs; used predecessor workflow to send 228 personalized cold emails and land 3 internship offers.

FridgeIt – Food Waste Reduction App

University of Missouri – Hackathon Project

Nov 2023 & Nov 2024

Columbia, MO

- Built a full-stack app (Python/Flask, recipe API, CRUD) to track fridge inventory and suggest recipes; placed 2nd at TigerHacks twice.
- Implemented user authentication and search filters collaborating in a 3-person team under 36-hour deadlines.

Biomedical Robotic Hand

Mechanical & Aerospace Engineering Dept., University of Missouri

Dec 2023 – Feb 2024

Columbia, MO

- Implemented Python signal-processing pipelines (FFT, filtering) for a robotic blood-pressure sensor; analyzed data from 20+ volunteer trials, improving sensor accuracy by ~20%.

LEADERSHIP

Secretary – Claude Builders Club – Mizzou Chapter

Feb 2026 – Present

President & Vice President – Uzbek Student Organization (UzSO)

Aug 2025 – Present; President since May 2026

Tech Committee Leader – Muslim Student Organization

2024 – Present

SKILLS & TECHNICAL COURSEWORK

Programming: Python, TypeScript/JavaScript, C/C++, SQL, MATLAB, R, HTML/CSS

AI & Computer Vision: PyTorch, OpenCV, DepthAI, scikit-learn, NumPy, Pandas, CNNs, U-Net, RAG, pgvector

Web & Backend: Next.js, React, Flask, Supabase, Postgres, Netlify Functions, Tailwind CSS, Git/GitHub, Linux

Electrical & Hardware: NI Multisim, PSpice, SolidWorks, AutoCAD, AutoCAD Electrical, Bluebeam Revu, ProjectWise, oscilloscopes, multimeters, soldering

Coursework: Microprocessor Engineering, Circuit Theory I & II, Circuits & Signals I & II, Logic Systems, Algorithm Design